Via monte Nero, 40/B – 21049 TRADATE (VA) ITALY
Phone: +39 (0)331841070 - e-mail:datexel@datexel.it - www.datexel.it

Intelligent Unit with Data-logger and Ethernet interface + Digital I/O

User Guide – MODBUS protocol

Firmware Version:
DAT9000IO-USB-2.0: 9BB5
DAT9000DL-IO-2.0: 9BB5

DAT9000-IO-USB-2.0
DAT9000DL-IO-2.0

All the data shared by a device communicating by Modbus RTU / Modbus TCP protocol are mapped in tables where in to each data is associated a determined address.
Each data could be of two types:
- “REGISTER”, data of 2 bytes size (word of 16 bits) that can be associated to analogue input or output, variables, set-point, etc...
- “COIL”, data of 1 single bit that can be associated to digital input or output or to a logic state.

A register could contain the image (mirror) of more coils; in example the 16 digital inputs of a device could be read or written as bit (singularly) addressing the coil related to each input or can be read or written as a single word addressing the associated register wherein each bit corresponds to a coil. In the Modbus protocol, registers and coils are divided as per the following groups of addresses:
0xxxx and 1xxxx = Coils (bit)
3xxxx and 4xxxx = Registers (word)
When addressing the registers when functions of reading and / or writing of registers and coils are used, use the tables shown below, adding 1 if the device is polled by external unit. The module's internal registers can be accessed via direct Modbus RTU or Modbus TCP command.

REGISTERS TABLE

Register	Description	Access
0	Status	RO
1÷2	Firmware Version	RO
3÷4	Name	R/W
5	Port 1 Set (RS485 Master)	R/W
6	Address	R/W
7	Port 1 Timeout	R/W
8	Digital Inputs	RO
9	Digital Outputs	R/W
10	System Flags	R/W
11	Power Up/Safe Digital Output	R/W
12	WatchDog Time	R/W
13	--Reserved--	RO
14	--Reserved--	RO
15	Reset Timers	R/W
16	COM Errors	R/W
17	Gateway Mask [L-H]	R/W
18	Port 0 Set (RS485 Slave)	R/W
19	Port 2 Set (uUSB Slave)	R/W
20	Timers Enable	R/W
21	--Reserved--	RO
22	RTC[Sec]	RO
23	RTC[Min/Hours]	RO
24	RTC[Day/Date]	RO
25	RTC[Month/Year]	RO
26÷34	--Reserved--	RO
35÷926	General Purpose (RAM)	R/W
927	Reset Counters	R/W
928	Frequency Digital Input 0	RO
929	Frequency Digital Input 1	RO
930	Frequency Digital Input 2	RO
931	Frequency Digital Input 3	RO
932÷933	Counter Digital input 0 (32 bit)	R/W
934÷935	Counter Digital input 1 (32 bit)	R/W
936÷937	Counter Digital input 2 (32 bit)	R/W
938÷939	Counter Digital input 3 (32 bit)	R/W
940÷959	--Reserved--	RO
960÷1023	General Purpose (RAM)	R/W
1024÷1025	--Reserved--	RO
1026	Ethernet. Comm. Slave Status	RO
1027	--Reserved--	RO
1028÷1045	Buffered RAM	R/W
1046÷1048	--Reserved--	RO
1049÷1215	General Purpose (RAM)	R/W
1216÷1219	--Reserved--	RO
1220	Slow Counters Enable	R/W
1221÷1222	--Reserved--	RO
1223	Min Pulse Duration	R/W
(*)1224÷1279	Retentive Registers (EEPROM)	R/W

COILS TABLE

Coil (Hex)	Coil (Dec)	Description	Access
0x0080	00128	Digital Input #0	RO
0x0081	00129	Digital Input #1	RO
0x0082	00130	Digital Input #2	RO
0x0083	00131	Digital Input #3	RO
0x0084	00132	Fall Latch #0	R/W
0x0085	00133	Fall Latch #1	R/W
0x0086	00134	Fall Latch #2	R/W
0x0087	00135	Fall Latch #3	R/W
0x0088	00136	Rise Latch #0	R/W
0x0089	00137	Rise Latch #1	R/W
0x008A	00138	Rise Latch #2	R/W
0x008B	00139	Rise Latch #3	R/W
0x0090	00144	Digital Output #0	R/W
0x0091	00145	Digital Output #1	R/W
0x00A0	00160	Watch-dog Enable	R/W
0x00A1	00161	Watch-dog Event	R/W
0x00A2	00162	Power-Up Event	R/W
0x00B0	00176	PowerUp Dig. Out #0	R/W
0x00B1	00177	PowerUp Dig. Out #1	R/W
0x00B8	00184	Watchdog Dig. Out #0	R/W
0x00B9	00185	Watchdog Dig. Out #1	R/W

SUPPORTED MODBUS FUNCTION CODES

Function	Description
01	Read Coil Status (0xxxx)
02	Read Inputs Status (1xxxx)
03	Read Holding Registers (4xxxx)
04	Read Inputs Registers (3xxxx)
05	Force Single Coil
06	Preset Single Register
15 (0F)	Force Multiple Coil
16 (10)	Preset Multiple Registers

NOTES:
The registers and coils marked in the 'Access' column with the words RO are read-only registers (Read Only).
The registers and coils marked in the 'Access' column with the words R/W are read and write registers (Read / Write).
(*) Pay attention to the use of retentive registers in EEPROM (1224-1279) as they can not be written continuously.

Functions 01, 02 and 15 support reading and writing up to a maximum of 32 consecutive coils.
The maximum number of registers that can be read through Modbus functions 03 and 04 (see "Supported modbus functions codes") are: 64
The maximum number of registers that can be written by Modbus function 16 (see "Supported modbus functions codes") are: 64

DESCRIPTION MODBUS REGISTERS

%R0 (40001) : STATUS

This read-only register indicates the device status. The values that this registry takes are:
Stop → 101
Halt / Step → 102
In all other cases (Debug, Run, Release and Animate) → 255

%R1/%R2 (40002 / 40003) : FIRMWARE VERSION

Field of 2 read-only registers, which contains the firmware identifier given by the manufacturer.
- Manufacturer's default: 9BB5 (ASCII)

%R3/%R4 (40004 / 40005) : NAME

Field of 2 registers (4 bytes or 4 ASCII characters) available to the user, can contain the name of the device or an acronym that identifies the function within the system. Each of the 4 bytes can contain any value from 0 to 255, thus also ASCII characters.
The default value of this field contains the module identifier in ASCII characters.
- Manufacturer's default: "9000" (ASCII).

%R5 (40006) : PORT 1 SET (RS485 Master)

In the lower part of this register it is possible to set the communication parameters (baudrate, parity and stop bit) relative to the serial port RS485 Master (Port 1).
- Manufacturer default: 38400 bps, parity NONE, stop bit 1

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Desc.	-	-	-	-	-	-	-	-	-	P1	P0	-	Sb	B2	B1	B0

Parity	P1	P0
None	0	0
Even	0	1
Odd	1	0

Stop bit	Sb
1	0
2	1

BaudRate	B2	B1	B0
1200	0	0	0
2400	0	0	1
4800	0	1	0
9600	0	1	1
19200	1	0	0
38400	1	0	1
57600	1	1	0
115200	1	1	1

%R6 (40007) : ADDRESS

Contains the Modbus address of the module; addresses from 1 to 249 are allowed.
Each module connected to the same network must have a unique address.
- Manufacturer's default: 10

%R7 (40008) : PORT 1 TIMEOUT

Indicates the value of the time within the device must wait for the answer from the slave devices connected to its RS485 Master port (Port 1).
Beyond this time, any answers that arrive will be ignored.
This value is expressed in milliseconds.
- Manufacturer's default: 100

%R8 (40009) : DIGITAL INPUT

This register shows the status of the digital inputs, the values of the Latch on the falling edge and the Latch on the rising edge (0 = OFF, 1 = ON).
The same inputs can also be read via the coils table, of which this register is a mirror; it is possible to use this register to read all the inputs simultaneously without having to implement the specific functions of read coils (01-02).

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Descr.	Fall Latch				Input								Rise Latch			
Input	#3	#2	#1	#0	#3	#2	#1	#0	-	-	-	-	#3	#2	#1	#0
Coil	135	134	133	132	131	130	129	128	-	-	-	-	139	138	137	136

%R9 (40010) : DIGITAL OUTPUT

This register allows to control directly the status of the output relays (0 = OFF; 1 = ON).
The same outputs can also be read or written via the coils table, of which this register is a mirror; it is possible to use this register to read / write all outputs simultaneously without having to implement the specific functions of read / write coils (01-02-05-15).

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Output	-	-	-	-	-	-	#1	#0	-	-	-	-	-	-	-	-
Coil	-	-	-	-	-	-	145	144	-	-	-	-	-	-	-	-

%R10 (40011) : SYSTEM FLAGS
Contains the enable bits and system events of the device. The following parameters are configurable:

WATCHDOG ENABLE
Enables the WatchDog alarm. If the alarm is enabled and the device doesn't receive commands for a time higher than the one specified in register %R12 (40013), the WatchDog Alarm will be activated (refer to section “Procedures”).
0 = Watchdog disabled.
1 = Watchdog enabled.

WATCHDOG EVENT
Indicates the state of the WatchDog Alarm. If the alarm is enabled and the device doesn't receive commands for a time higher than the one specified in register %R12 (40013), this bit is forced to 1. To erase the alarm set this bit to 0. If the bit is forced to 1 by a command of the Master unit, a Watchdog event will be simulated and consequently an alarm condition will be created.
0 = Normal condition
1 = Alarm condition

POWER-UP EVENT
This bit is forced to 1 each time the device is powered-on in order to indicated that the device has been switched-off or a reset is occurred. By the set of this bit to 0 and check its state it is possible to monitor if a reset of the device is occurred.
0 = reset not occurred
1 = reset not occurred

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Set	-	-	-	-	-	162	161	160	-	-	-	-	-	-	-	-

Enable Watchdog Event
Watchdog Event
Power-up Event

%R11 (40012) : WATCHDOG AND POWERUP DIGITAL OUTPUTS (COILS)
At power up (PowerUp) and in case of Watchdog alarm (Safe), the outputs are automatically set to the corresponding value expressed in this register. Each bit of the register corresponds to a digital output, according to the table below:
- Manufacturer's default: 0

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Descr.					PowerUp								WatchDog (Safe)			
Output	-	-	-	-	-	-	#1	#0	-	-	-	-	-	-	#1	#0
Coil	-	-	-	-	-	-	177	176	-	-	-	-	-	-	185	184

%R12 (40013) : WATCHDOG TIMER
Contains the value of the WatchDog timer expressed in seconds. If the WatchDog is enabled and the module does not receive commands for a time equal to the value contained in this register, the WatchDog alarm is triggered (see description in the "Procedures" section). The device return back from the alarm at the first command has been received.
- Manufacturer's default: 10 (10 sec)

%R15 (40016) : RESET TIMERS
Each bit of this register is associated with the reset of the relative internal timer (register %R20). If the bit relative to a specific timer is set to 1 while it is active and before it reaches the end of the countdown, the timer count is resetted and restarts from 0 msec. The reset timer bit will be automatically reset by the system.

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Descr.	Reset Timers															
# Timer	T7	T6	T5	T4	T3	T2	T1	T0	T15	T14	T13	T12	T11	T10	T9	T8

%R16 (40017) : COM ERRORS
Counter of communication errors on the Master port. The value of this register is incremented each time a Modbus command is sent to the master port and no response is received.

%R17 (40018) : GATEWAY MASK [L-H]
In this register it is possible to specify the range of modbus addresses that the controller can consider as valid in relation to the commands sent through its own Master port when used as a Gateway. When the controller operates as a gateway and it is in stop condition, if it sends queries to a device whose address is not included in the mask, the command will be ignored. This register, appropriately configured, can eliminate echo problems on the RS485 network. The Gateway Mask is ignorated if the addresses are interrogated directly by the controller through an internal project.
- Manufacturer's default: 255 (00FF Hex)

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Descr.	Initial Address Mask								Final Address Mask							

%R18 (40019) : PORT 0 SET (RS485 Slave)
%R19 (40020) : PORT 2 SET (uUSB)

In the upper part of this register it is possible to set the communication parameters (baudrate, parity and stop bit) relative to the serial port RS485 Slave (Port 0) and of the virtual serial port uUSB Slave (Port 2). The communication parameters are common for both slave ports (Port 0 and uUSB).
Warning: the uUSB port and the RS485 slave port (Port 0) cannot be used simultaneously.
The lower part of the register is reserved for the Delay RX / TX of the slave port and indicates the time delay that the device waits before transmitting the response to the connected master unit. The value can be between 1 and 255 and is expressed in milliseconds.
- Manufacturer default: 38400 bps, parity NONE, stop bit 1, Delay RX / TX slave 1

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Desc.	-	P1	P0	-	Sb	B2	B1	B0	Delay RX/TX							

Parity	P1	P0
None	0	0
Even	0	1
Odd	1	0

Stop bit	Sb
1	0
2	1

BaudRate	B2	B1	B0
1200	0	0	0
2400	0	0	1
4800	0	1	0
9600	0	1	1
19200	1	0	0
38400	1	0	1
57600	1	1	0
115200	1	1	1

WARNING! To communicate through the uUSB Slave port (Port 2) it is mandatory to use the supplied CVPROG cable.

The CVPROG cable is not a common USB-uUSB cable but has a chip inside it that generates a virtual port on your PC.

%R20 (40021) : TIMERS ENABLE

Each bit of this register is associated to an internal timer. The timer starts to count when the relative bit is set to 0. After the time specified for the timer has expired (through the proper Function Block in Dev9k), the same bit is automatically forced to 1, indicating that the timer has expired.

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Descr.	Timers															
# Timer	T7	T6	T5	T4	T3	T2	T1	T0	T15	T14	T13	T12	T11	T10	T9	T8

%R22 (40023) : RTC[Sec]
%R23 (40024) : RTC[Min/Hours]
%R24 (40025) : RTC[Day/Date]
%R25 (40026) : RTC[Month/Year]

Field of 4 read/write Registers: contain the value of the internal clock.

The following information are available:

Seconds	00 ÷ 59
Minutes	00 ÷ 59
Hours	00 ÷ 23
Day of week	01 ÷ 07 (01=Sunday, 02=Monday, ... 07=Saturday)
Date	01 ÷ 31 (it depends on the month)
Month	01 ÷ 12 (01=January, 02=February, ... 12=December)
Year	00 ÷ 99 (00=2000, 99=2099)

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
%R22	---								Seconds [0÷59]							
%R23	Minutes [0÷59]								Hours [0÷23]							
%R24	Day of week [1÷7]								Date [1÷31]							
%R25	Month [1÷12]								Year [0÷99]							

NOTE: all of the values are written as hexadecimal characters .
NOTICE! Writing these Registers will imply the variation of the clock and calendar settings.

%R35 (40036) → %R926 (40927) : General Purpose (RAM)
%R960 (40961) → %R1023 (41024) : General Purpose (RAM)
%R1049 (41050) → %R1215 (41216) : General Purpose (RAM)

These three blocks of registers are areas of RAM that can be used by the user for the construction of their projects. The user, through these registers can perform mathematical operations, logical operations or store data from external devices with the use of specific modbus functions (reading, writing). These registers can be written continuously.

ATTENTION: these registers are in RAM memory therefore, in the absence of power supply to the device, the contained values will be lost.

%R927(40928) : RESET DIGITAL COUNTERS

This register allows reset digital counters. To reset the counter, the bit relative to that counter must be forced to 1.
Write the register to force the bits according to the following table:

Counter #	Value (Dec)	Value (Hex)
0	256	100
1	512	200
2	1024	400
3	2048	800

To reset 2 or more counters at the same time, enter in this register the sum of counter values to reset:
Write 768 (300 Hex) to reset counters # 0 and # 1
Write 1536 (600 Hex) to reset counters # 1 and # 2
Write 1792 (700 Hex) to reset counters #0, #1 and #2
Write 3804 (F00 Hex) to reset contaters #0, #1, #2 and #3

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Input	-	-	-	-	#3	#2	#1	#0	-	-	-	-	-	-	-	-

Reset Counter #0
Reset Counter #1
Reset Counter #2
Reset Counter #3

%R928 (40929): FREQUENCY DIGITAL INPUT 0
%R929 (40930): FREQUENCY DIGITAL INPUT 1
%R930 (40931): FREQUENCY DIGITAL INPUT 2

These registers show the frequency measurement relative to each digital input channel.
The values are exspressed in Hz and the registers format are *Unsigned Integer 16 bit*.
The frequency range that can be measured is from 1 Hz to 5 kHz when the slow counters are **disabled** (refer to register %R1220).
The frequency range that can be measured is from 0,1 Hz to 300,0 Hz when the slow counters are **enabled** (refer to register %R1220).

The slow counters can be enabled or disabled by the integrated web server or through the %R1220 register.

%R931 (40932): FREQUENCY DIGITAL INPUT 3

This register show the frequency measurement relative to digital input channel 3.
The value is expressed in tenths of Hz and the register format is *Unsigned Integer 16 bit*.

Maximum measurable frequency 300.0 Hz

%R932 (40933) -%R933 (40934) [LO-HI] : COUNTER DIGITAL INPUT 0 (32 BIT)
%R934 (40935) -%R935 (40936) [LO-HI] : COUNTER DIGITAL INPUT 1 (32 BIT)
%R936 (40937) -%R937 (40938) [LO-HI] : COUNTER DIGITAL INPUT 2 (32 BIT)

These registers show the measurement of the pulse counter associated to the digital input channels 0, 1 and 2. The value is incremented at each change of level from 0 to 1 for each input channel. The type of register is an *Unsigned Long 32 bit*.
It is possible to reset the counters using the register %R927 (Reset Counters).
When the slow counters are enabled, the **"Debouncing"** function is associated to them and the the maximum applicable signal frequency is 300 Hz.
The minimum acceptable pulse value ("Debouncing") is defined in the register %R1223.
This registers are in retentive RAM memory area, therefore, in case of a power supply failure, the values contained in these registers are not lost.
When the slow counters are disabled, the maximum applicable signal frequency is **5 kHz**.

The slow counters can be enabled or disabled by the integrated web server or through the %R1220 register.

%R938 (40939) -%R939 (40940) [LO-HI] : COUNTER DIGITAL INPUT 3 (32 BIT)
These registers show the measurement of the pulse counter associated to the digital input channel 3. The value is incremented at each change of level from 0 to 1 of the input channel. This counter is only a slow counter. The type of register is an *Unsigned Long 32 bit*.
This counter is associated with the **"Debouncing"** function whose minimum acceptable pulse value is defined in register %R1223.
It is possible to reset the counter using the register %R927 (Reset Counters).
This register is in retentive RAM memory area, therefore, in case of a power supply failure, the value contained in this register is not lost.
The maximum applicable signal frequency is **300.0 Hz**.

%R1026 (41027): Ethernet Comm. Slave Status

This register indicates which of the slave devices inserted in the IP table does not communicate (Modbus TCP protocol).
Each bit corresponds to a device in the same position in the IP table.
- If the bit = 1 → the device not communicates
- If the bit = 0 → the device communicates or there isn't in the IP table

Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
Device	-	-	-	-	-	-	-	-	#7	#6	#5	#4	#3	#2	#1	#0

→ Position in the IP table:
Device 0 → bit 0 → position 0

%R1028 (41029) → %R1045 (41046) : Buffered RAM

These registers are in an area of RAM memory that is powered by the internal buffer battery, which is why they are called "Buffered".
They are like the registers in RAM General Purpose but, in case of absence of power supply to the device, the contained value is not lost because it is maintained by the internal battery. These registers can be written continuously.

%R1220 (41221) → Enable Slow Counter

This register allow to enable the slow counters for digital inputs 0, 1 and 2.
0 → Slow counters disabled (fast counters enabled)
1 → Slow counters enabled (fast counters disabled)
When the slow counters are enabled, it is possible to set the "Debouncing" value for all counters by register %R1223 or by webserver.

%R1223 (41224) : Min Pulse Duration (Only Slow Counters/Counter 3) - Debouncing

This register indicates the minimum duration of the impulse that can be accepted expressed in ms. The maximum value accepted is 255 ms. This register refers to the "Debouncing" function of counter 3 or when the slow counters are enabled (counter 0, counter 1, counter 2). The value can be set either by the web server or by writing with the "Write single register" function of the standard modbus (function 06).
Note: this parameter is only available if slow counters are used.

%R1224 (41225) → %R1279 (41280) : Retentive Registers (EEPROM)

These registers are in EEPROM and therefore called retentive. In case of a power supply failure, the value contained in these registers will not be lost. They are often used to store constants that are used in several function blocks during the making of the project by the Dev9k software or to store values that must be not continuously changed (from a SCADA, HMI or PLC).

ATTENTION: these registers are in EEPROM memory therefore they can not be used to be written continuously. If this occurs, the EEPROM would be irreparably compromised because limited cycles of writing are allowed.

HOW TO CONNECT IN INIT MODE

USING THE "INIT" FUNCTION

All DAT9000 series devices are equipped with the INIT mode. This is a mode to access the device with the default parameters and then to configure it:

Over Ethernet:

- IP Address: XXX.XXX.XXX.XXX provided by the DHCP if is enabled or 192.168.1.174 if DHCP is disabled (verify that the IP is not already used and that the PC belongs to the same subnet)
- Modbus address: 10

By these parameters it is possible to access the device in INIT mode to configure it or see the stored configuration.
To enter INIT, follow the procedure below:

- Turn off the device;
- Connect the INIT terminal to the -V terminal as shown in the technical datasheet of the device.
- Turn on the device;
- Connect to the device using the default parameters shown above.

When the user finishes working in INIT mode:

- Turn off the device;
- Remove the INIT connection;
- Turn on the device and connect with the parameters known or configured in INIT mode.

Over RS485 slave or uUSB:

- Switch off the device.
- Connect only the device to be programmed to the RS485 slave or uUSB network.
- Connect the INIT terminal to terminal V-.
- Turn on the device.

- Set the communication port with the following values

Mode = Modbus RTU
baud-rate = 9600 bps
parity = None
bit number = 8
stop bit = 1

- The form answers at address 10.
- Read or program the desired settings in the registers using the Dev9k software.
- Switch off the device.
- Disconnect the INIT terminal from terminal V-.
- Set the communication port with the programmed baud-rate
- The module answers with the programmed address

WATCHDOG

The modules of the DAT9000 series are provided with the Watchdog timer which, if enabled, triggers an alarm every time the communication between the module and the master remains inactive for a time longer than the specified one.
During the Whatchdog alarm state, the green "PWR" LED on the front of the module starts to flash and the "Watchdog Event" coil is set to 1. To exit the alarm condition, send any command to the device and reset the "Watchdog Event" coil.

FUNCTION BUTTON “P”

In case of necessity, the “P” button allow to the user to recover the “Factory parameters” in to the device.

With the device on and not in INIT condition, press the button “P” located on the front for at least 5 seconds;
The green led PW will switch-off, the led STS will become orange and the reset of the device will occur (modbus parameters, default IP, login credentials to the web server).
When the reset is finished, both LEDs will return to the default state condition for which the following parameters will be loaded:

Ethernet:
- IP Address: 192.168.1.100
- Subnet Mask : 255.255.255.0
- Gateway Mask: 192.168.1.1

Modbus:
- Address : 10
- Baud Rate Port 1: 38400 bps
- Baud Rate Port 0 and Port 2: 38400 bps

Login:
- Username: Fact_user
- Password: Fact_pwd

ATTENTION: do not switch off the device during the loading phase!

INTERFACE CABLE “CVPROG”

Description

The CVPROG cable is an interface consisting of the physical cable, a uUSB port that must be connected to the DATEXEL device in use, a USB port that must be connected to the user PC and a chip to recognize the USB port as VCP (Virtual Com Port).
Due to this the CVPROG interface cable is not a simple uUSB-USB cable.
Through the CVPROG cable it is possible to communicate and program the DATEXEL devices without external power.
This allows a simple use of the device.
WARNING: the uUSB port and the RS485 slave port (Port 0) cannot be used simultaneously and the communication parameters are common to both ports.
When connecting the CVPROG cable to the PC, it will be necessary to install the drivers supplied with the CDROM supplied with the device or downloaded from the website www.datexel.it

Check of the generated COM port

When the CVPROG cable is inserted into the PC, a virtual COM port is automatically generated and it can be displayed in the "Device Management" window → Ports (COM and LPT) of the operating system in use.

Factory uUSB port parameters (not INIT mode)

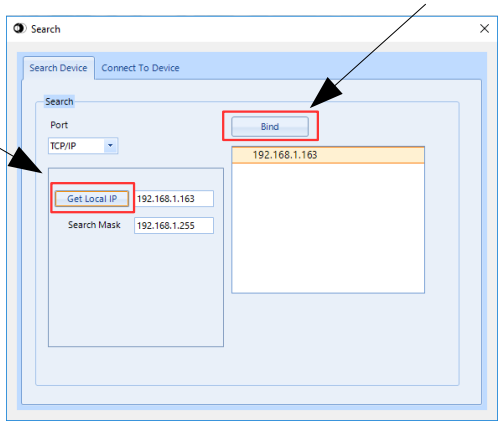
Baud rate: 38400 bps
N°bit: 8
Parity: None
Stop bit: 1

SEARCH FOR A DEVICE IN THE NETWORK

The DAT9000 series devices have the default IP address 192.168.1.100 and modbus address 10.
The PC can work with multiple subnets.
Note: To find the controller, the PC and the DAT9000 series device must be part of the same subnet.

Fig. 1

Fig. 2



Please follow this procedure to connect to DAT9000 device:

1) Run the Dev9k 2.0 and click on the *"Connect"* button in the Home Page (Fig. 1)
2) Check if the IP address of device is in the same subnet of the PC.
   Click on *"Get Local IP"* button to display the PC IP address (Fig. 2)
3) In the area under the *"Bind"* button, the networks available on PC are displayed.
   Select, if listed, the linked network to which the IP address belongs.
4) Click on *"Bind"* button to set the network to search for the device (Fig. 2)
5) Click on *"Search"* button to search the device in the network (Fig. 3)
6) Select the IP address of the controller, press the right mouse button and select *"Set as Controller"* to connect to the desired device.
7) When to the message box "*Connected"* appears, click on "OK".

Fig. 3

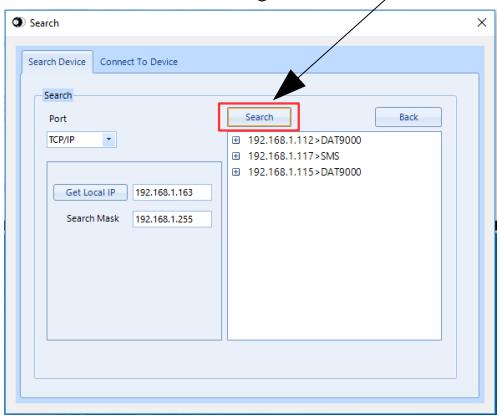
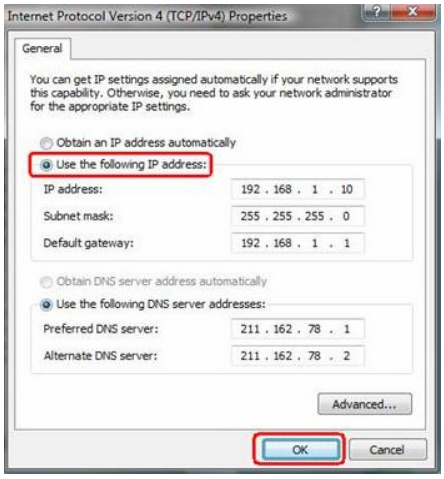


Fig. 4



If the device does not belong to the same subnet of the PC, in order to change the IP of the product, first change the IP of the Personal Computer so that the device belongs to the same subnet of the PC.
To do this, open the *"Network and Sharing Center"* section of the operating system in use, go to the PC IPV4 network properties window (Fig. 4) and configure the network parameters compatibly with the default IP of the device: (see section "*Examples for Windows® configurations"*)

IP Address: 192.168.1.XXX; modbus address (xx)
Subnet Mask: 255.255.255.0
Gateway Mask: 192.168.1.1

After carrying out the previous operations, the DAT9000 device can be searched on the network and it is possible to change the IP address with the desired one, which must be different from the other IPs used in the network.

To change the device IP address and the other network parameters, follow the procedure below:
1. Connect to the device (point from 1) to 7) of the previous procedure)
2. In the menu bar, click on *Tools* → *Config*, it will appears the box in figure below (Fig. 5)
3. Click on the tag *"Ethernet"* and change the network parameters (Fig. 6)
4. Click on to edit the parameters (Fig. 6)

Fig. 5

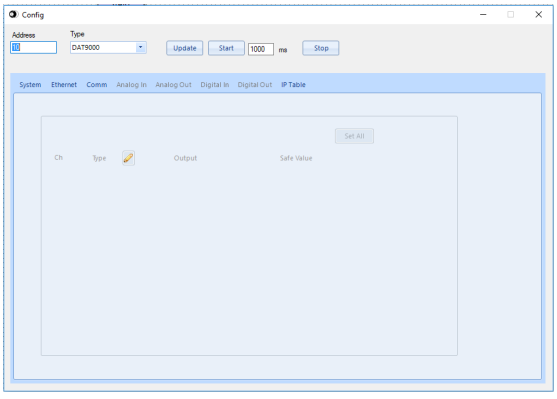
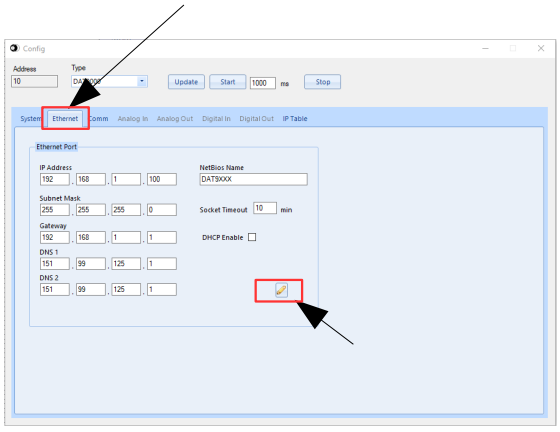


Fig. 6



Once the IP address of the Datexel device has been changed, it will no longer appear in the search results in the Dev9k. The user will then need to re-edit the PC network parameters to display the device again between the search results.

Datexel s.r.l. reserves its right to modify the characteristics of its products totally or in part without warning at any time.

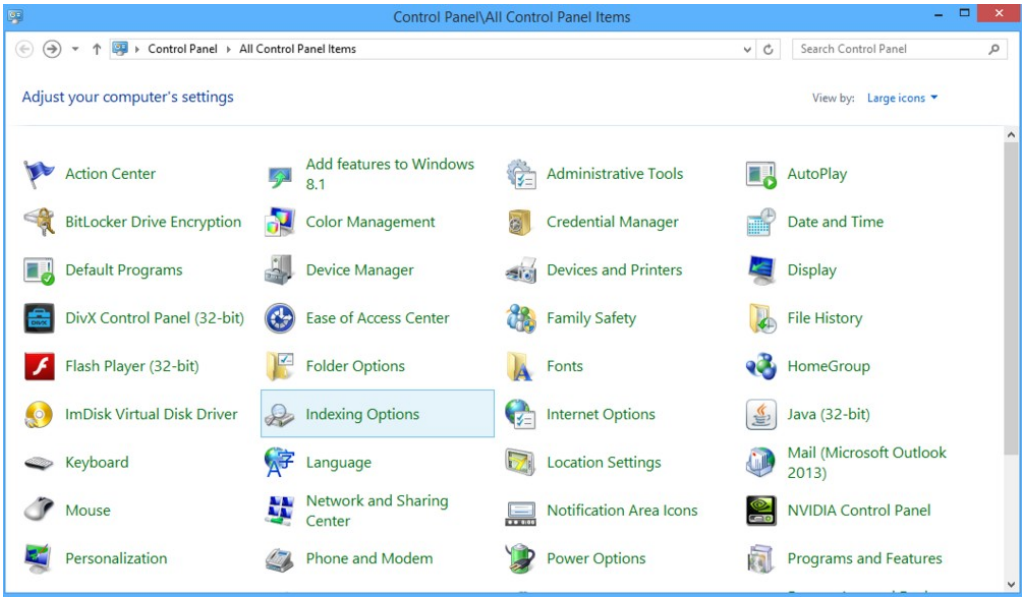
ADDITIONAL COMMANDS TO DETERMINE THE NETWORK IN USE

It is possible to use the following additional commands to determine to which network is connected the PC.
To use it run the command Prompt (cmd.exe) **as Administrator** in the Windows in use .
Command “Ipconfig”
It is possible to visualize the nets used by the PC writing this command and press enter.
The system will return a list of all the nets used by the PC. Before to try to establish a communication with the device the user must be sure to be in the correct subnet (ref previous chapter).
Command “ping”
To determine if a device is connected on the net it is possible to use the "ping " command that is an utility for computer networks used to measure the time, in milliseconds, used by one or more packets to reach a network device and to return to the origin.
To use the command , type the command " ping " followed by the IP address of the device and press Enter .
Example:
ping 192.168.1.100
If the device is connected the system will return the answer from the device with the IP mentioned.
If the systems returns the message Host Unreachable the device is not connected in the net in use.
In this case it is suggested to check the network assigment.

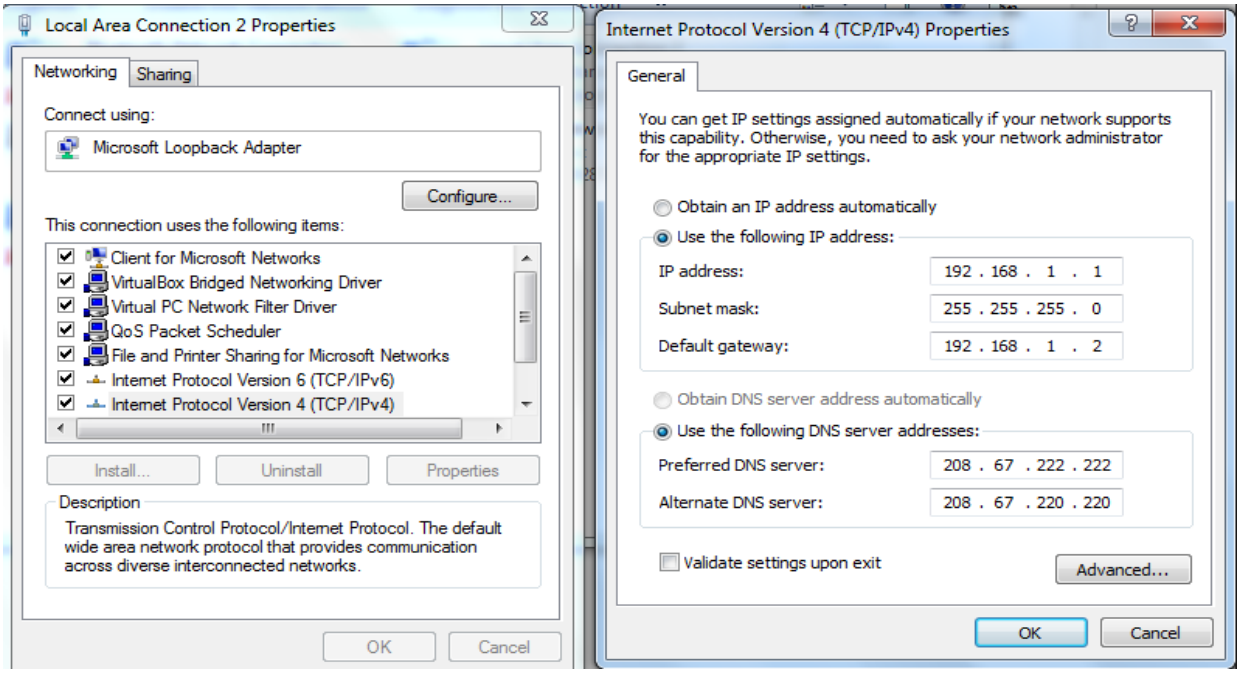
EXAMPLE TO CHECK WINDOWS ® CONFIGURATION

How to change the IP address of the PC (each version of the operative system could change in part from others)

- Access to Control Panel of PC



- Network and Sharing Center→ Change Adapter Settings → select the interested network → right click → Properties → Internet Protocol version 4 → Properties.



- Change parameters and click OK.

RIF. FW : 9BB5
ED.02.21 REV.05